

2312-0866-2 PCT



21/11-12-02
C. Moore

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

KIMIHIRO MATSUSE ET AL. : GROUP ART UNIT: 2814

SERIAL NO: 09/530,588 :

CPA FILED: Herewith : EXAMINER: QUACH, T.

FOR: WIRING STRUCTURE OF SEMICONDUCTOR DEVICE, ELECTRODE, AND METHOD FOR FORMING THEM

RECEIVED
TC 2800 MAIL ROOM
NOV -6 2002

PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Responsive to the Official Action of June 4, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION

Page 1, please replace line 1 to read as follows:

TITLE OF THE INVENTION

Page 1, line 2, please delete the title in its entirety and substitute therefor:

METHOD OF FORMING SEMICONDUCTOR WIRING STRUCTURES

IN THE CLAIMS

Please cancel claims 63-88 without prejudice or disclaimer.

Please add new Claims 89-111.